TOSHIBA Photocoupler IRED & Photo-Transistor

# **TLP631, TLP632**

Programmable Controllers AC/DC-Input Module Solid State Relay

The TOSHIBA TLP631 and TLP632 consist of a photo-transistor optically coupled to an infrared emitting diode in a six lead plastic DIP. TLP632 has no-base internal connection for high-EMI environments.

• Collector-emitter voltage: 55 V (min)

• Current transfer ratio: 50% (min)

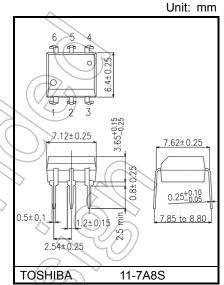
Rank GB: 100% (min)

• Isolation voltage: 5000 Vrms (min)

UL-recognized: UL 1577, File No.E67349

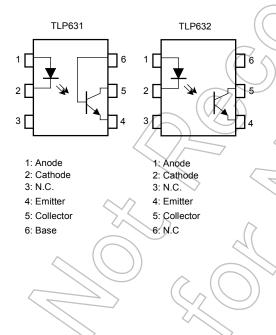
cUL-recognized: CSA Component Acceptance Service No.5A

File No.E67349



Weight: 0.4 g (typ.)

### Pin Configurations (top view)



Start of commercial production 1983-05

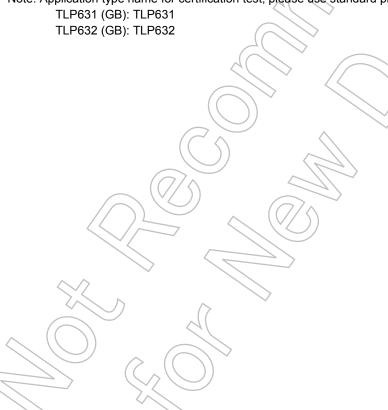


### **Current Transfer Ratio**

Current Transfer Ratio (%) (Ic/IF)			
Classification (Note 1)	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V, Ta = 25°C		Marking Of Classification
	Min	Max	
Blank	50	600	Blank, Y <sup>*</sup> , YE, G, G <sup>*</sup> , GR, B, BL, GB
Rank Y	50	150	YE, Y
Rank GR	100	300	GR, G, G <sup>■</sup>
Rank BL	200	600	BL, B
Rank GB	100	600	GB, GR, G, G <sup>●</sup> , BL, B
Rank YH	75	150	Y• ( ) >
Rank GRL	100	200	G
Rank GRH	150	300	G• (( )
Rank BLL	200	400	B

Note 1: Ex, rank GB: TLP631 (GB)

Note: Application type name for certification test, please use standard product type name, i, e.





### Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	lF	60	mA
	Forward current derating (Ta ≥ 39°C)	ΔI <sub>F</sub> /°C	-0.7	mA/°C
ED	Peak forward current (100 µs pulse, 100 pps)	IFP	1	A
쁘	Reverse voltage	V <sub>R</sub>	5	V
	Diode power dissipation	PD	70	mW
	Diode power dissipation derating (Ta ≥39 °C)	ΔP <sub>D</sub> /°C	-0.82	mW/°C
	Collector-emitter voltage	VCEO	55	(V)
	Collector-base voltage (TLP631)	V <sub>CBO</sub>	80	V
or	Emitter-collector voltage	VECO	7	) v
Detector	Emitter-base voltage (TLP631)	V <sub>EBO</sub>	7	V
Ď	Collector current	Ic	50	mA _
	Power dissipation	Pc (	150	mW
	Power dissipation derating (Ta ≥ 25°C)	ΔP <sub>C</sub> /°C	-1.5	mW/°C
Stor	rage temperature range	T <sub>stg</sub>	-55 to 125	°¢/
Оре	erating temperature range	Topr	-55 to 100	CC
Lead soldering temperature (10 s)		Tsol	260	~e/
Tota	al package power dissipation	RT	250 (///	∫mW
Tota	al package power dissipation derating (Ta≥ 25°C)	ΔP <sub>T</sub> /°C	-2.5	mW/°C
Isola	ation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)	BVS	5000	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Device considered a two terminal device: LED side pins Shorted together and DETECTOR side pins shorted together.

# **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	Vcc	_	5	24	V
Forward current	lF	_	16	25	mA
Collector current	Ic	_	1	10	mA
Operating temperature	T <sub>opr</sub>	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



## **Electrical Characteristics (Ta = 25°C)**

	Characteristic		Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	IF = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5V	_	_	10	μΑ
	Capacitance	Ст	V = 0 V, f = 1 MHz	<u> </u>	30	_	pF
	Collector-emitter breakdown voltage	V <sub>(BR)</sub> CEO	I <sub>C</sub> = 0.5 mA	55		_	V
	Emitter-collector breakdown voltage	V(BR) ECO	IE = 0.1 mA	Z	)'-	_	V
or	Collector-base breakdown voltage (TLP631)	V <sub>(BR)</sub> CBO	I <sub>C</sub> = 0.1 mA	80	-	_	V
Detector	Emitter-base breakdown voltage (TLP631)	V(BR) EBO	I <sub>E</sub> = 0.1 mA	7	_	_	V
	Collector dark current	loco	V <sub>CE</sub> = 24 V	_	10	100	nA
	Collector dark current	ICEO	V <sub>CE</sub> = 24 V, Ta = 85 °C	_	(2)	50	μA
	Capacitance collector to emitter	CCE	V = 0 V, f = 1 MHz	-52	10	, –	pF

# Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	lo/le	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V	50	_	600	%
	Ic/I <sub>F</sub>	Rank GE	100	1	600	/0
Saturated CTR	lo/ls ( t)	IF = 1 mA, VCE = 0.4 V	_	60	1	%
Saturated CTR	IC/IF (sat)	Rank GE	30	1	1	70
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 2.4 mA, I <sub>F</sub> = 8 mA	_	1	0.4	٧





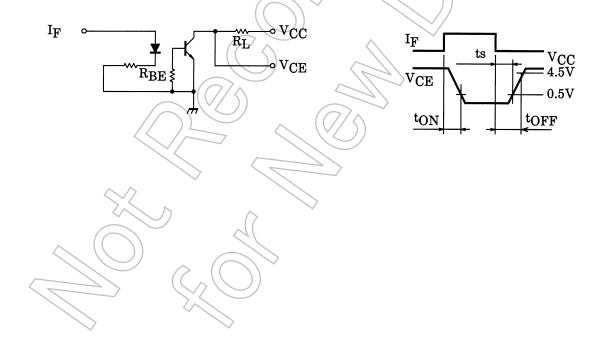
## **Isolation Characteristics (Ta = 25°C)**

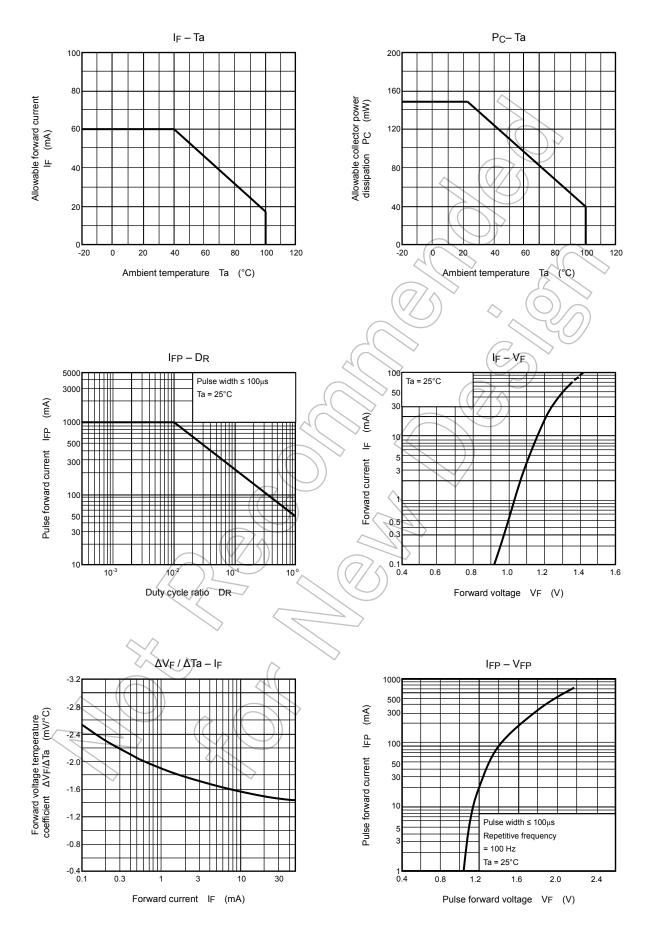
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	Cs	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	5×10 <sup>10</sup>	10 <sup>14</sup>	1	Ω
Isolation voltage	BVs	AC, 60 s	5000	1		Vrms

### **Switching Characteristics (Ta = 25°C)**

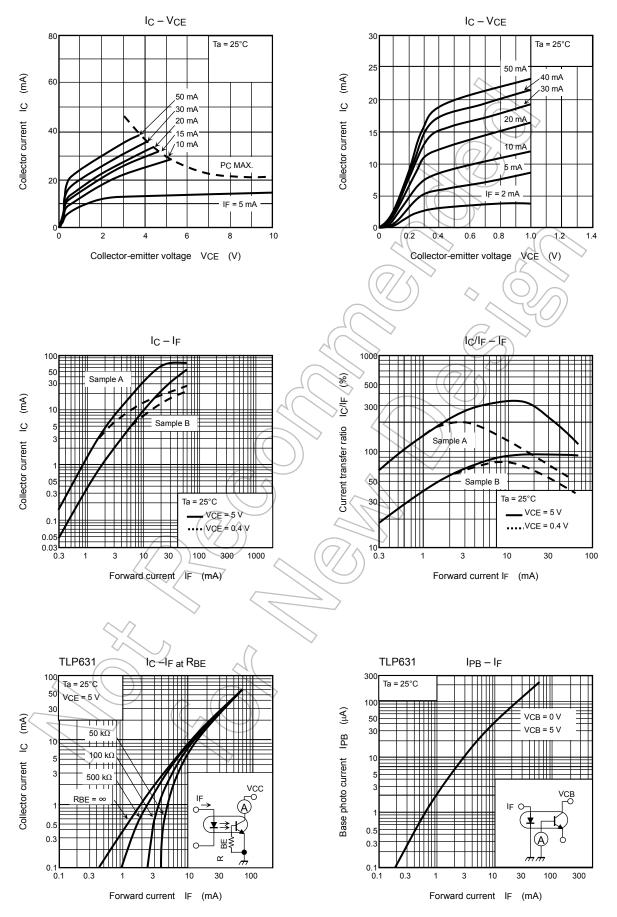
Characteristic	Symbol	Test Condition Min Typ. Max	Unit
Rise time	t <sub>r</sub>	- 2 -	μs
Fall time	t <sub>f</sub>	V <sub>CC</sub> = 10 V, I <sub>C</sub> = 2 mA	
Turn-on time	t <sub>on</sub>	R <sub>L</sub> = 100Ω	
Turn-off time	t <sub>off</sub>	- 3 -	
Turn-on time	ton	$R_L = 1.9 \text{ k}\Omega$ (Fig.1)	
Storage time	t <sub>s</sub>	R <sub>BE</sub> = OPEN	μs
Turn-off time	toff	Vcc = 5 V, IF = 16 mA	
Turn-on time	ton	2 -	
Storage time	ts	$R_{L} = 1.9 \text{ k}\Omega$ (Fig.1) $R_{BE} = 220 \text{ k}\Omega$ (TLP631) 12 $-$	μs
Turn-off time	toff	VCC = 5 V, IF = 16 mA	

Fig. 1 Switching time test circuit

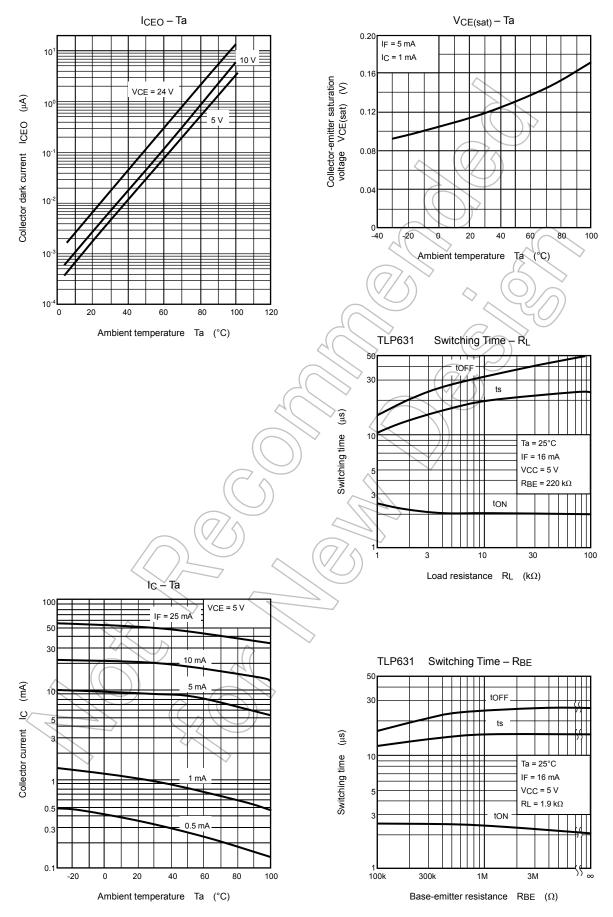




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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